

AMENDMENTS TO THE SPECIFICATION

The disclosure is objected to on page 21 of the specification as not having the incorporated by reference section update. Applicants herewith amend page 21, paragraph [0083] as follows:

[0083] Incorporated by reference herein are all above references, patents, or applications and the following U.S. applications, which are assigned to the assignee of this application: application no. 10/371,220 entitled DATA SYNCHRONIZATION ACROSS AN ASYNCHRONOUS BOUNDARY USING, FOR EXAMPLE, MULTI-PHASE CLOCKS (attorney docket no. 37179.8001); application no. 09/989,590 entitled HIGH-SPEED BUS WITH EMBEDDED CLOCK SIGNALS (attorney docket no. 37179.8003); application no. 09/989,587 entitled MULTI-PHASE VOLTAGE CONTROL OSCILLATOR ("VCO") WITH COMMON MODE CONTROL (attorney docket no. 37179.8005); application no. 09/989,645 entitled SYSTEM AND METHOD FOR MULTIPLE-PHASE CLOCK GENERATION (attorney docket no. 37179.8006); application no. 10/043,886 entitled CLOCK AND DATA RECOVERY METHOD AND APPARATUS (attorney docket no. 37179.8011); application no. 09/989,647 entitled LOGIC GATES INCLUDING DIODE-CONNECTED METAL-OXIDE-SEMICONDUCTOR FIELD-EFFECT TRANSISTORS (MOSFETS) TO CONTROL INPUT THRESHOLD VOLTAGE LEVELS AND SWITCHING TRANSIENTS OF OUTPUT LOGIC SIGNALS (attorney docket no. 37179.8022); and application no. 09/989,487 entitled DIFFERENTIAL AMPLIFIERS USING ASYMMETRIC TRANSFER CHARACTERISTICS TO SUPPRESS INPUT NOISE IN OUTPUT LOGIC SIGNALS (attorney docket no. 37179.8023). Aspects of the invention can be modified, if necessary, to employ the systems, functions and concepts of the various patents and applications described above to provide yet further embodiments of the invention.